



PATENT

- 1 -

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Reissue Application of

PAI-HSIANG KAO, et al.

Patent No. 6,023,094

Issued: February 8, 2000

Reissue Application No.: 10/022,683

Filed: December 17, 2001

For: **SEMICONDUCTOR WAFER HAVING  
A BOTTOM SURFACE PROTECTIVE  
COATING**

Group Art Unit: 2815

Examiner: S. Clark

**AUTHORIZATION BY  
ASSIGNEE TO EXECUTE  
REISSUE DECLARATION  
AND POWER OF ATTORNEY**

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Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450


Sir:

As set forth in the **Consent by Assignee to Filing of Reissue Application and Offer to Surrender Patent** relating to the subject application and filed on December 17, 2001, the undersigned (whose title is supplied below) is authorized to act on behalf of the Assignee, National Semiconductor Corporation, in connection with the subject application. I further represent that Allen R. Tremain was authorized by National Semiconductor Corporation to execute the **Reissue Application Declaration by the Assignee** on March 30, 2001 with the sole exception of the provision in the **Declaration** relating to the appointment of attorneys and agents to prosecute the subject application. I have been informed that this was an inadvertent error on the part of Mr. Tremain. I further represent that I am so authorized and I hereby appoint the following attorney(s) and/or

agent(s) to prosecute this application and transact all business in the United States Patent and Trademark Office connected herewith:

Name(s):	Registration Number:
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Dated: 12/9/03

By:   
Name: Christopher J. Byrne

Title: Director of Intellectual Property and Technology Licensing

Attorney Docket No. NSC1-G9900